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tive to the voltage present between the two collectors.

113. The inverter circuit of Claim 112 wherein another capacitor is connected between the two collectors in parallel with said series-combination to reduce the high frequency voltage components in the inverter output.

N4. The inverter circuit of Claim 112 in which the load is a gas discharge lamp.

115. A ballast for a fluorescent lamp, said ballast including an inverter for providing a substantially squarewave AC voltage output across two output terminals, comprising:

an inductor and a capacitor connected in series across said two output terminals, said inductor and capacitor in combination having a natural resonant frequency that is lower than the fundamental frequency component of said squarewave AC voltage; and

means for connecting the fitorescent lamp in parallel circuit with said capacitor, said squarewave AC voltage being transformed thereby in waveshape and amplitude to provide a substantially sinusoidal voltage of appropriate magnitude to start and operate the lamp.

REMARKS

The foregoing amendment has been made because claims 56-85 were added by mistake due to an inadvertent miscommunication between the applicant and his attorney. The set of claims intended to be added by applicant are claims 86-115 above.

Respectfully submitted,

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